

In the Specification:

Please amend the last paragraph on page 5 as follows:

B1 [0020] Whether on the motherboard or on the memory modules, the buffers may be implemented using more than one buffer component per memory module. Also, although this example embodiment shows four memory modules, other embodiments may include other numbers of memory modules. Further, other DRAM device configurations and number of DRAM components may be supported in other embodiments. Other embodiments are possible where the DRAM is placed on a motherboard rather ~~that~~ than on a memory module. Still other embodiments are possible using types of memory devices other than DRAM.